

#6

Fig. 1a

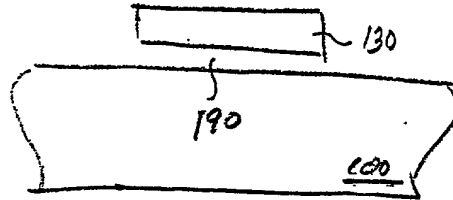


Fig. 1b

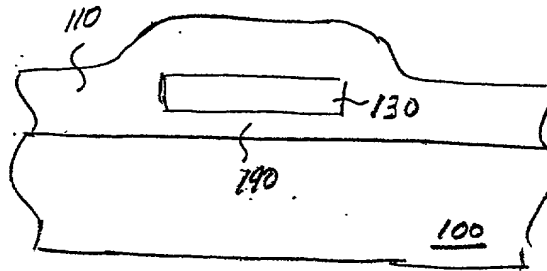


Fig. 1c

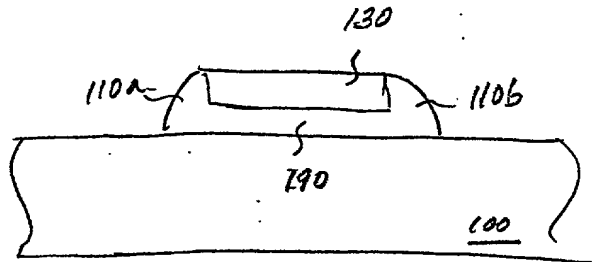


Fig. 1d

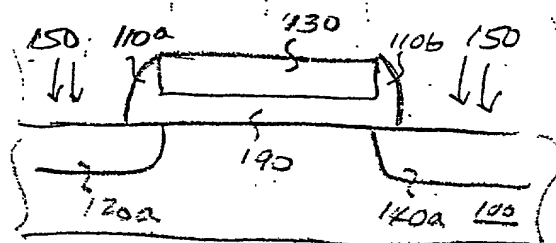


Fig. 1e

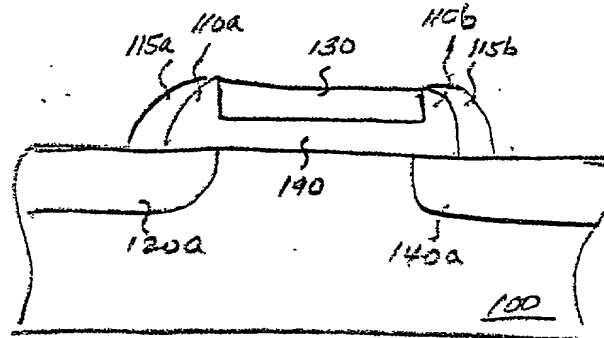


Fig. 1f

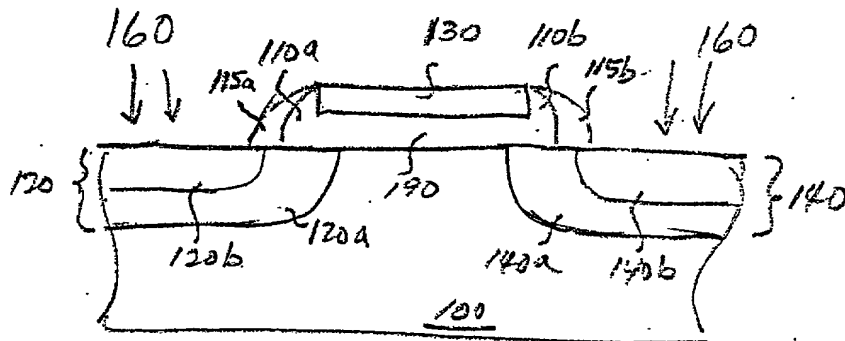
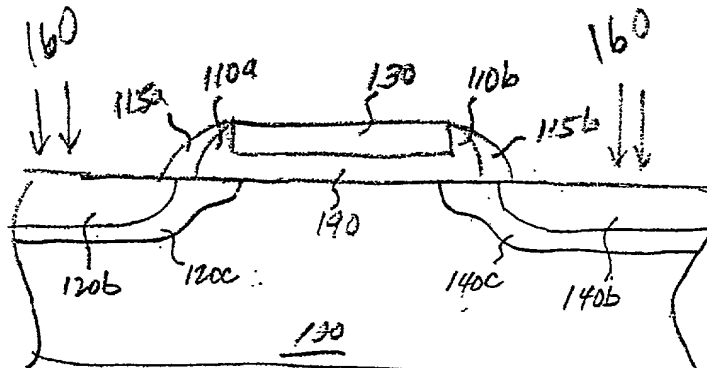
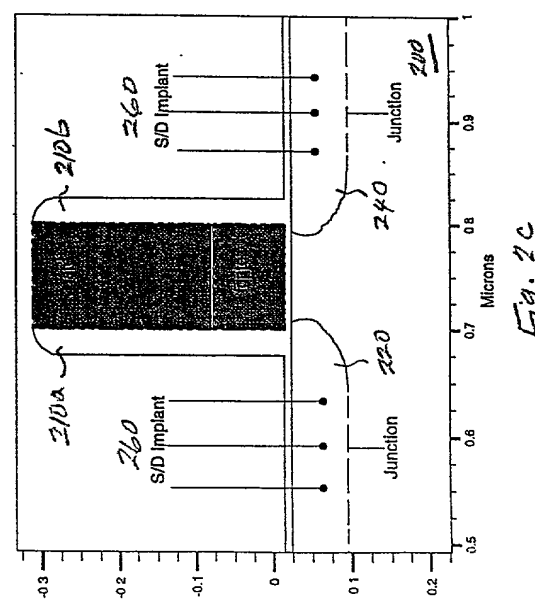
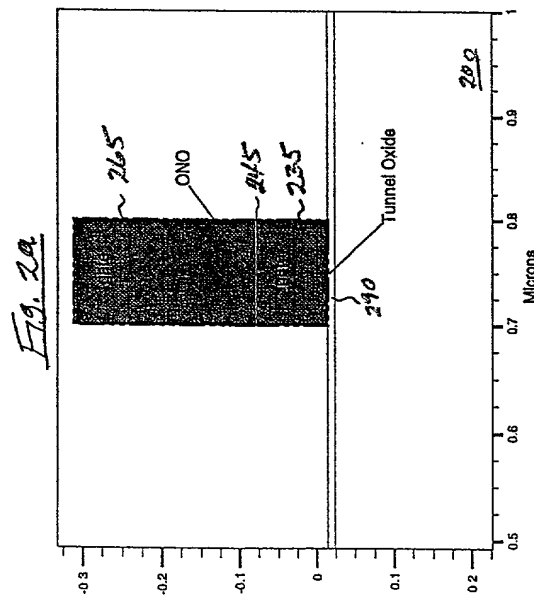
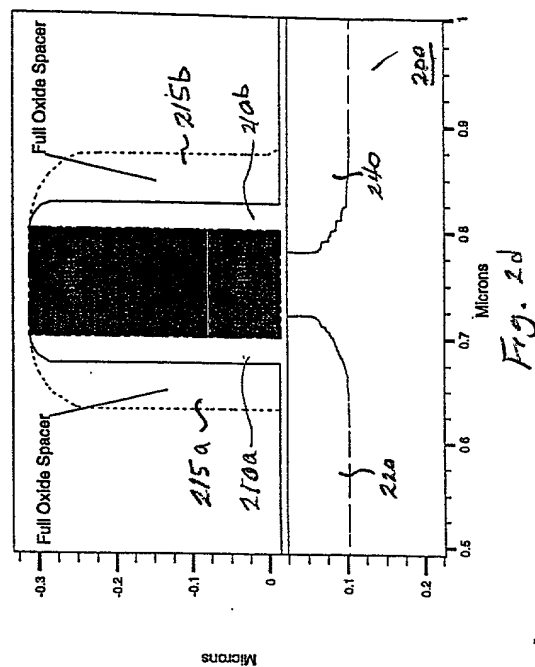
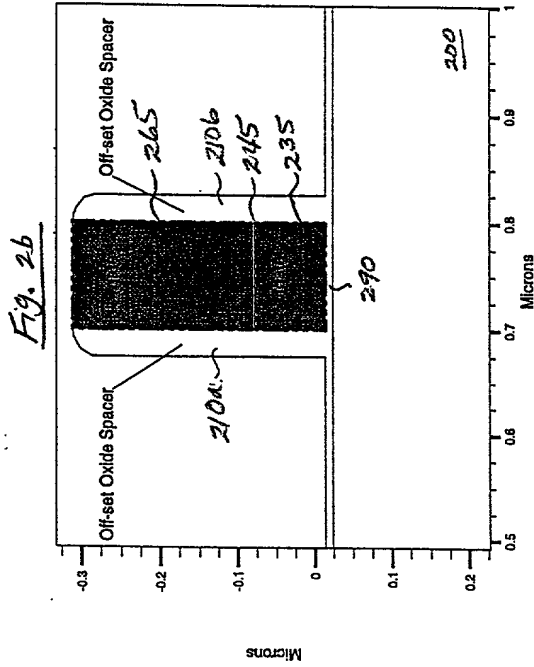


Fig. 1g





Array Cell

Fig. 3a



Fig. 3b



Fig. 3c

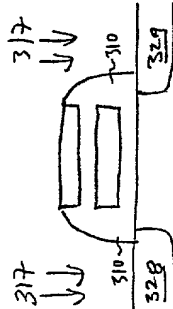
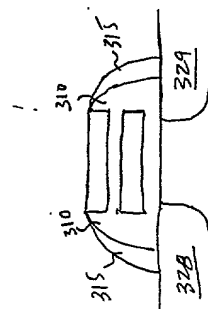
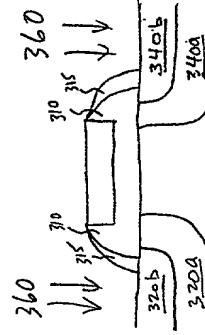
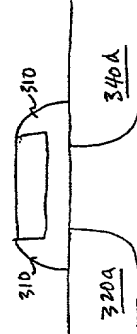
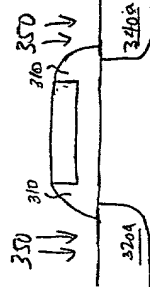


Fig. 3d



Periphery DDD
HV transistor



Periphery LDD
LV transistor

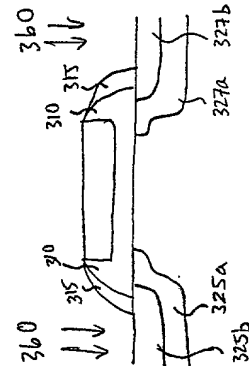
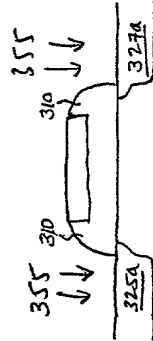
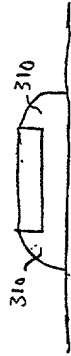


Fig. 4a

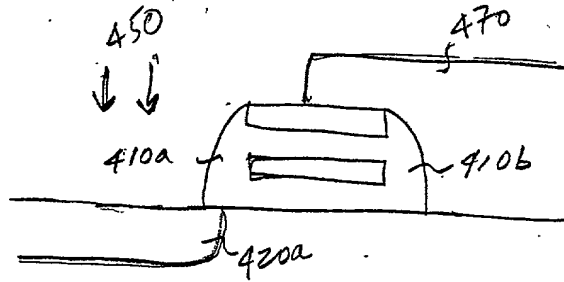


Fig. 4b

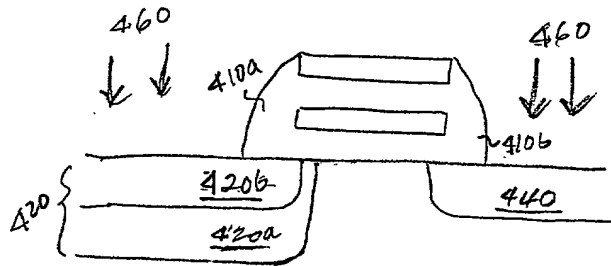


Fig. 5a

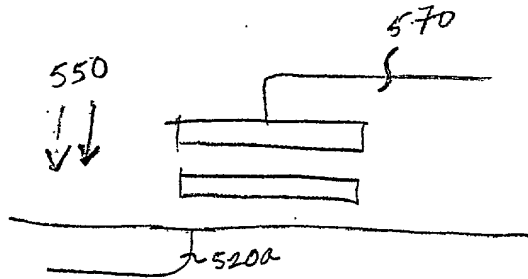
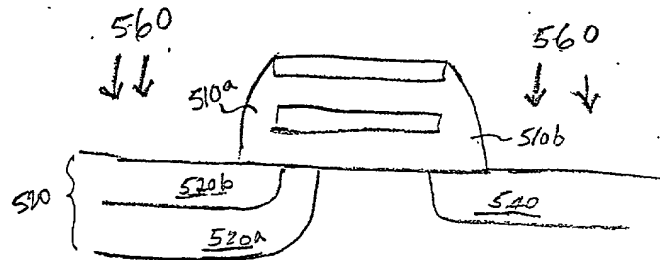


Fig. 5b



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Fig. 7.

